

ZP.VST.3463.A-SPECIFICATION

NO.(引脚)	Symbol(定义)	Description(描述)
1	VSEL	Power Supply for Panel
2	VSEL	
3	VSEL	
4	GND	Ground(地)
5	GND	
6	NC	NC
7	TX00-	LVDS ODD 0- Signal
8	TX00+	LVDS ODD 0+ Signal
9	TX01-	LVDS ODD 1- Signal
10	TX01+	LVDS ODD 1+ Signal
11	TX02-	LVDS ODD 2- Signal
12	TX02+	LVDS ODD 2+ Signal
13	GND	Ground(地)
14	GND	
15	TX0C-	LVDS ODD Clock- Signal
16	TX0C+	LVDS ODD Clock+ Signal
17	TX03-	LVDS ODD 3- Signal
18	TX03+	LVDS ODD 3+ Signal
19	TXE0-	LVDS EVEN 0- Signal
20	TXE0+	LVDS EVEN 0+ Signal
21	TXE1-	LVDS EVEN 1- Signal
22	TXE1+	LVDS EVEN 1+ Signal
23	TXE2-	LVDS EVEN 2- Signal
24	TXE2+	LVDS EVEN 2+ Signal
25	GND	Ground(地)
26	GND	
27	TXEC-	LVDS EVEN Clock- Signal
28	TXEC+	LVDS EVEN Clock+ Signal
29	TXE3-	LVDS EVEN 3- Signal
30	TEX3+	LVDS EVEN 3+ Signal

Pin No.	Symbol	Signal	Remarks
1	VCC	Power supply	.
2	VCC		
3	GND	Ground	.
4	GND		
5	D0-	Pixel data	Note2
6	D0+		
7	GND	Ground	.
8	D1-	Pixel data	Note2
9	D1+		
10	GND	Ground	.
11	D2-	Pixel data	Note2
12	D2+		
13	GND	Ground	.
14	CLK-	Pixel clock	Note2
15	CLK+		
16	GND	Ground	.
17	D3-	Pixel data	Note2
18	D3+		
19	GND	Ground	.
20	MSL	Selection of LVDS input Map Note1	High: Input map A mode Low or Open: Input map B mode

А ЭТО НАДО ПОДКЛЮЧИТЬ
Матрица SVA150XG04TB